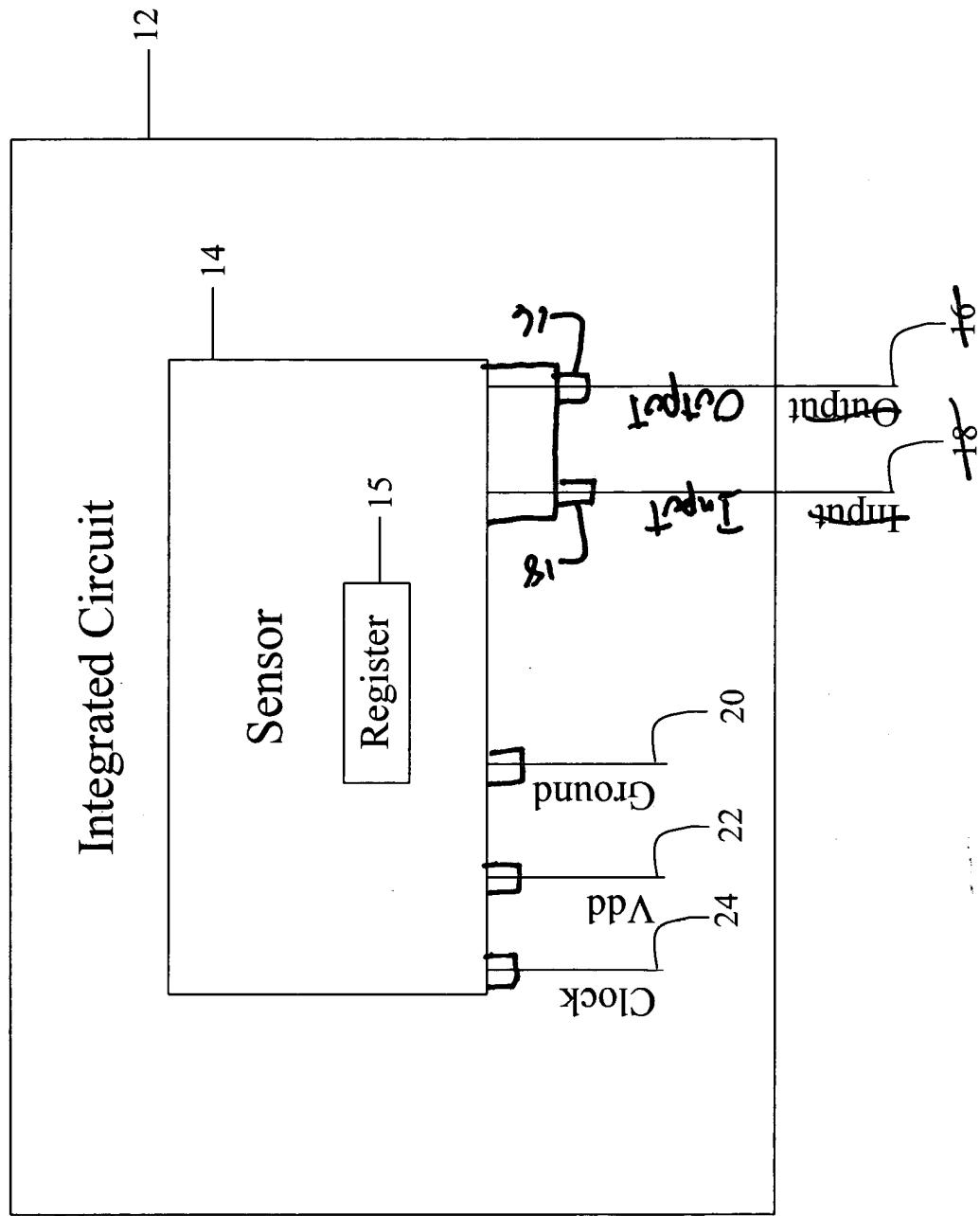


Figure 1



Figure 1





App No.: 09/935,385
Amendment dated 10/17/03
Reply to Office Action of 06/17/03
REPLACEMENT SHEET

Clock Signal 37

Timing diagram illustrating the relationship between Input Signal 30 and Output Signal 32.

Input Signal 30: A digital signal with the following steps:

- Step 1: 1 → 0 (at time 21)
- Step 2: 0 → 1 (at time 33)
- Step 3: 1 → 0 (at time 35)

Output Signal 32: A digital signal with the following steps, labeled "sensing":

- Step 1: 0 → 1 (at time 21)
- Step 2: 1 → 0 (at time 33)
- Step 3: 0 → 1 (at time 35)

Fig. 2

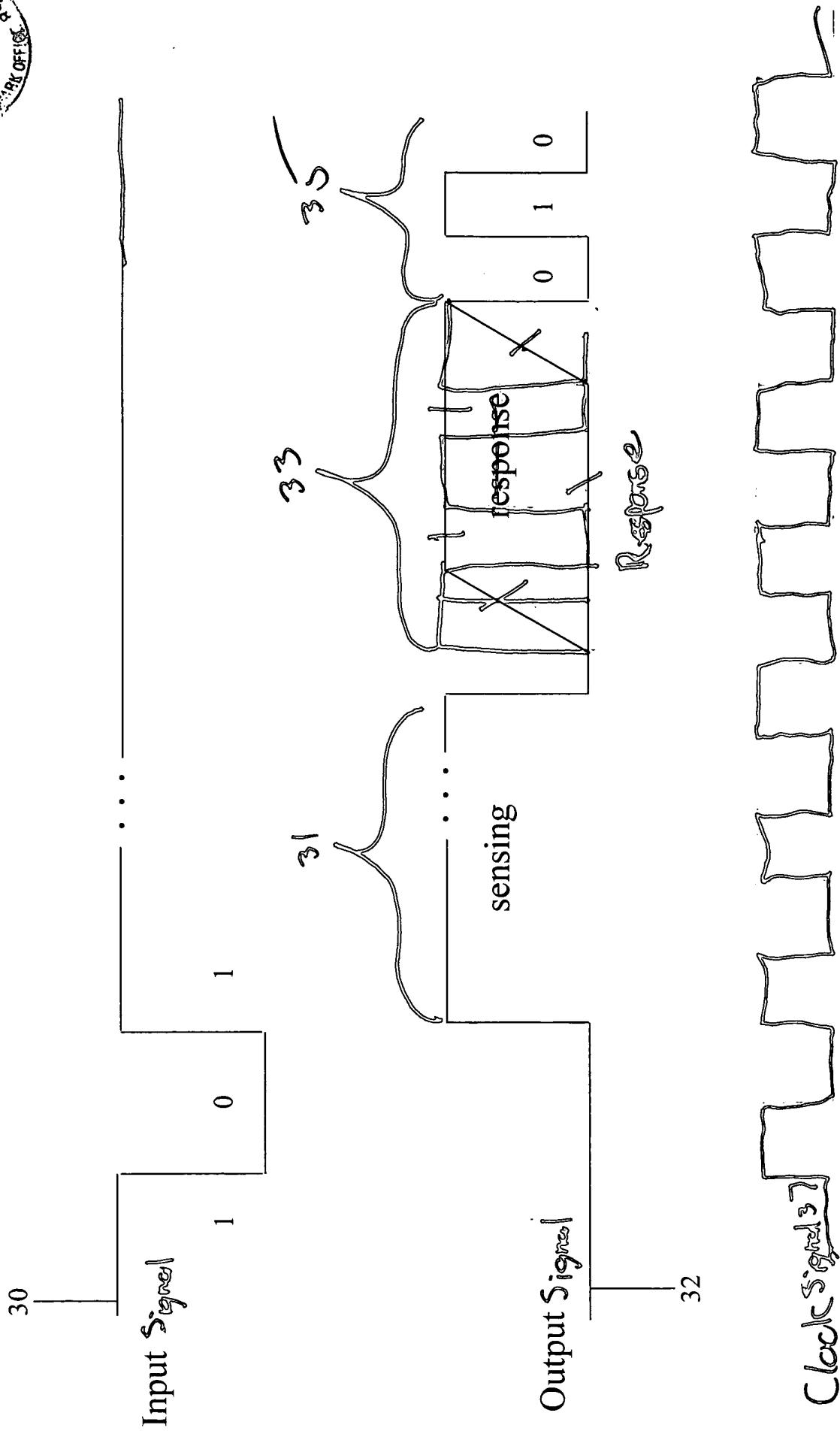
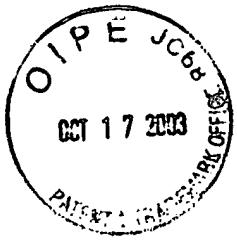


Figure 2